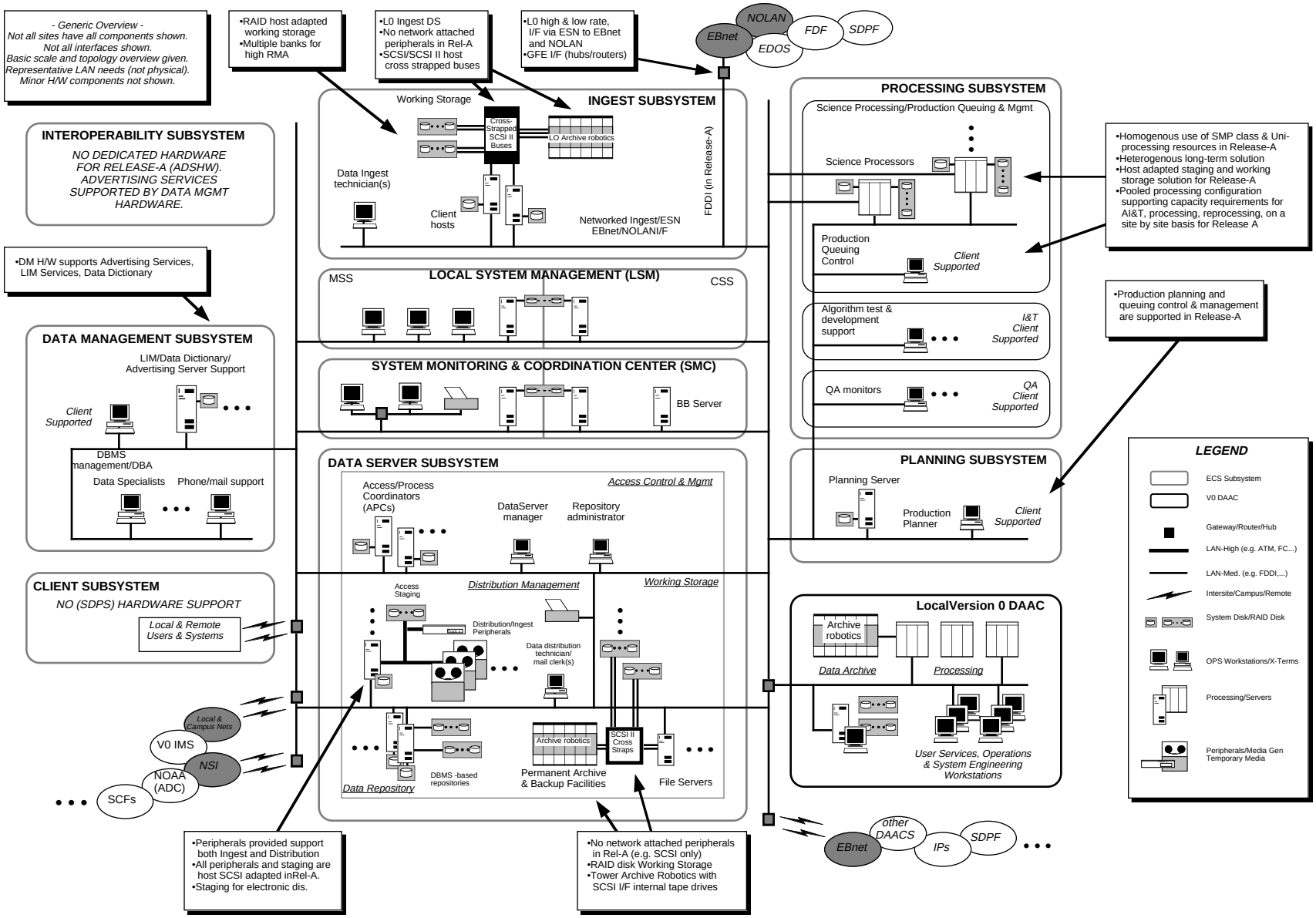

Figure 5.4-1 ECS DAAC Release A Hardware Architecture